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Origins of Conduction at Domain Boundaries, $\text{LaAlO}_3/\text{SrTiO}_3$ and Surface for Depolarization & Size Effect

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Abstract— The conduction at domain boundaries due to ferroelectric polarization and $\text{LaAlO}_3/\text{SrTiO}_3$ found by Ohtomo and Hwang [Nature 427, 423 (2004)] are intriguing. If these conduction are different from the conventional conduction at domain boundaries and oxide interfaces due to defects, they prove the earlier predictions [Phys. Rev. Lett. 86, 332(2001); Phys. Rev. B57, 789(1998)]. That is, when these conduction are primarily due to ferroelectric polarization as predicted, the foundations of mesoscale and nanoscale ferroelectrics should change. Considering conventional mechanisms including the high field effect as in the resistance switching (RRAM), we examine their origin and discuss these implications.

Keywords—conduction; ferroelectric; interface; domain; 2D electron; RRAM; electron layer; nano

I. INTRODUCTION

The carrier layers at insulator interfaces such as $\text{SrTiO}_3/\text{LaAlO}_3$ found in 2004 [1], domain boundaries [2-4], and surface of ferroelectrics (Fig.1) are intriguing topics. If and only if these carrier layers are primarily due to polarization discontinuity, i.e., ferroelectric polarization P_s as predicted and partly demonstrated in 2001 [5, 6], they have fundamental impacts on the basis of ferroelectricity. The examples are super-relaxation of size effect and domain configuration [5-7].

Although the predictions of Fig. 1 were confirmed by *ab initio* calculations [8, 9], the experimental results [1-5,10,11] can be due to conventional mechanisms, as evidenced in the failure of formation of both electron e^- and hole h^+ layer at the same locations by low field. Here, conduction originating from defects at domain boundaries [12-15] and the interfaces of heterostructures [16-18] and in bulk [19-25] have been established. High-field induced defects are also known [26, 27] in the studies of resistance switching. Therefore, we need to examine these experimental results without relying on theories.

II. INSULATIVITY AND SIZE EFFECT

Good ferroelectrics are good insulators without defects. This is because the conductance σ of ferroelectrics degrades their dielectric and ferroelectric response: σ adds a large imaginary part of dielectric constant $\epsilon_i = \sigma/2\pi f\epsilon_0$ where f and ϵ_0 are frequency and vacuum permittivity, respectively. Similarly, defects yields large ϵ_i and apparent real part of dielectric constant. Also, the switching of P_s is hindered by σ .

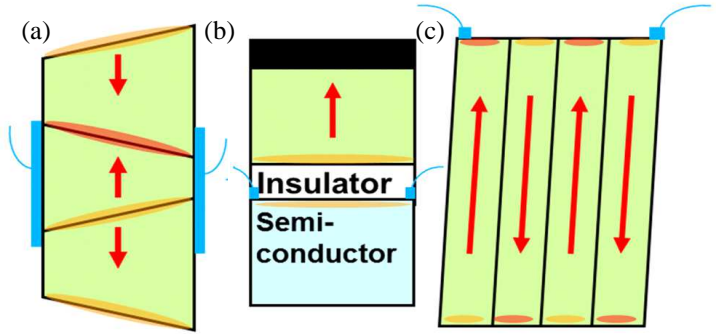


Fig. 1 Typical situations of free carrier layers by polar discontinuity i.e., ferroelectric field effect, where arrows and shades show P_s of ferroelectric and carrier layer, respectively (adopted from Phys. Rev. Lett. 86, 332 (2001) [5]): (a) head-to-head or tail-to-tail domain boundaries, (b) ferroelectric/insulator interface, whereas this figure expresses ferroelectric/insulator in the limit of the infinite thickness of the insulator layer in the middle, (c) clean damage-free surface, where vacuum be an ideal insulator. (b) and (c) were predicted [6]. (c) was experimentally verified in ref. [5,7]. These experiments and the theory [5,6] led to the generalization that includes (a). The theoretical calculation [5, 7, 37] showed the thickness of the layer was approximately 1~3 nm, which was later experimentally confirmed by $\text{SrTiO}_3/\text{LaAlO}_3$ [1].

Therefore, theories should treat ideal ferroelectrics as defect-free insulators, when they consider the fundamental ferroelectric properties.

By regarding ferroelectric as an ideal insulator, Batra et al. [28] predicted the inexistence of thin single-domain ferroelectric for the incomplete screening of the depolarization field, i.e., the field from P_s . This initiated the “size effect by depolarization field”, which is considered established. Likewise, the depolarization field imposes strict restriction on the width of parallel domain, i.e., 180° or *cc* domains, which are vortex domain in the classical Kittel model [29].

The applications of this approach predict unconditional instabilities of ferroelectricity in almost any single-domain thin ferroelectric [30-32]. In particular, nanodomains (~3nm) were predicted to form abruptly for $l_d > 1.25\epsilon_d$ nm (Fig. 2, l_d : separation between electrode and ferroelectric, ϵ_d : its relative permittivity) [31]. This disagrees with partial experimental successes of ferroelectric FET's, because typical ferroelectric FET has SiO_2 -equivalent insulator thickness (l_d) of 10nm and the ferroelectric thickness of 300 nm and uses ferroelectric less resistive to the depolarization field instability than PbTiO_3 .

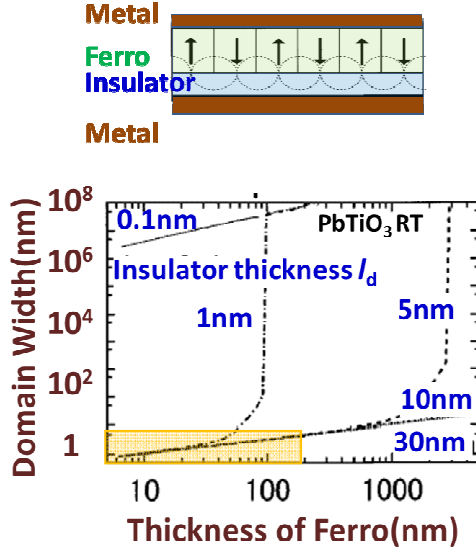


Fig. 2 Domain size vs. thickness of ferroelectric for different SiO_2 ($\epsilon_f = 3.9$) thickness l_d , where the total free energy is minimized in the structure shown in the sketch (adopted from J. Appl. Phys. 83, 2179 (1998) [31]). PbTiO_3 was used in this calculations, because was one of the ferroelectrics that were practical and the most resistive to the depolarization field instability. In the orange shaded region, free energy calculations show that ferroelectricity disappears at RT for $l_d \geq 1.25\epsilon_d$ nm. The compulsory nanodomain formation means that large domains for memory are absolutely not allowed, which seems to contradict with experiments. Therefore, this paper concluded that one of the basic assumptions, i.e., perfect insulativity with an infinite band gap was incorrect, although this prediction was later used for the explanation of fatigue by Bratkovsky and Levanyuk [34].

Moreover, even with nanodomains and PbTiO_3 -- depolarization-field-resistive --, ferroelectricity itself becomes unstable for practical range of parameters (orange shade in Fig. 2). Its unphysicalness was unnoticed [34], when only the fatigue in capacitors was studied. Another cause of the unawareness can be an inappropriate permittivity, i.e. the use of background permittivity for depolarization field [35], while it should be unity in practical cases [31-33].

III. EFFECT OF FINITE BAND GAP OF FERROELECTRIC

These difficulties originate from too strong depolarization field. However, when the electric field in insulators is too strong, the effect of the finite band gap must be examined. From this view, a Ginzburg-Landau theory [5] was presented, where the sole novelty was the rigorous formulation of ferroelectric with a finite band gap under self-consistent electric field.

This showed (1) existence of free electron e^- or free hole h^+ layer at polar discontinuities (Fig. 1) [5, 6]: ferroelectric/insulator interface, ferroelectric free-surface, and charged domain boundaries, (2) diminished depolarization instability and size effect by screening of these carriers, and, therefore, (3) freer domain configurations. The example of (3) is the stable existence of the domains in Fig. 1(a) even without defects, and the example of (2) is the stability of P_S in Fig. 1(b).

The prediction (2) is recently named as hyper-ferroelectricity. However, in our view, this property appears in many ferroelectric, this name may not be necessary.

IV. ELECTRON/HOLE LAYER BY POLARIZATION DISCONTINUITY

The experimental observations of the above prediction (1) “ e^-/h^+ layer at polar discontinuities” are insufficient to verify the correctness of the theories [5-7, 36-38]. This is because these theories treated ideal ferroelectrics where the effects of defects were insignificant, whereas e^-/h^+ layer originating from defects [13-25] are prevailing owing to experimental difficulties.

Moreover, the experimental observation of electrical conduction at relatively high electric field does not mean necessarily the existence of free e^-/h^+ layer. This is because space charge limited conduction and Poole-Frenkel conduction do not need preexisting free e^-/h^+ layer. When free e^-/h^+ layer preexists throughout the current path, ohmic conductance should be observed at very low electric field. In addition, high electric field creates defects that can produce conducting region along the field [26, 27].

Considering these basics, we examine the experimental observations. Ohmic conductance at low field is absent in the reported domain boundary conduction [13-15]. This shows that little free e^-/h^+ preexists there but the barrier of injection such as band gap is lowered there. In addition, the low mobility of these domain boundaries indicates the existence of defects working as pinning. Actually, these domain boundaries are formed by high electric field along the current path, which tends to create defects there [26,27].

Therefore, the existing domain boundary conduction would be probably due to the combined effects of polar discontinuities, defect formation by high field, and carriers from the defects. For example, from a careful study Farokhipoor and Noheda concluded that the conduction of natural domain boundaries of BiFeO_3 thin film was due to defects [14]. Consequently, domain boundary conduction so far are not used to prove the e^-/h^+ layer by polar discontinuities at domain boundaries (Fig. 1(a)).

One of the most elaborate insulators interfaces exhibiting carrier layer, as evidenced in ohmic conduction is the $\text{LaAlO}_3/\text{SrTiO}_3$ interface [1]. Because SrTiO_3 becomes ferroelectric by strain [39, 40], it is natural to attribute the electron layer to P_S of strained SrTiO_3 . However, the failure of the formation of hole layers suggests the predominance of the defects such as oxygen vacancies. Indeed, its growth requires high temperature ($\sim 600^\circ\text{C}$) in vacuum, where oxygen vacancies and intermixing of elements are unavoidable. This was also experimentally proved [10, 11].

Nonetheless, recent studies found the substantial contribution of P_S of SrTiO_3 in the electron layer formation [41-43] that has the electron mobility similar to that of Si. This mechanism is different from the original explanation of “polar catastrophe of LaO/AlO_2 layers”. Therefore, the electron layer in supports partially the earlier predictions (Fig. 1(b)) [5,6]. Here, LaAlO_3 and SrTiO_3 are regarded just as a simple insulator with appropriate lattice parameters and ferroelectric induced by strain, respectively.

Free ferroelectric surface in ultrahigh vacuum (UHV) is an idealized model of insulator/ferroelectric interface, which is intrinsically well defined as immobile edge of ferroelectric.

Although PbTiO_3 and BiFeO_3 have a high P_s , they consist of volatile Pb and Bi, which evaporate even in air above 400°C and are cause of a high conductance and opaqueness of their single crystals. To avoid these sources of extrinsic conductivity, stoichiometric BaTiO_3 single crystals were chosen from >3000 BaTiO_3 crystals in Remeika lab [5]. A benefit of flux-grown BaTiO_3 single crystals is the atomically flat surface without polishing that causes surface damage.

Accidentally, we were able to control the P_s direction without application of electric field on the conduction path (thickness direction in Fig. 1(c)). In addition, no change of resistivity along the poling field (thickness direction in Fig. 1(c)) was found [5]. Therefore, field induced conductivity [26, 27] along the surface in Fig. 1(c) was eliminated [5].

An advantage of free surface over insulator interface is the elimination of high temperature process and high energy atoms that cause defects and intermixing. Also, the oxygen vacancy formation in UHV is suspected, but the measurement of surface resistance of BaTiO_3 single crystals kept in UHV at RT were unchanged over 1 year, confirming the absence of oxygen vacancies at RT. The maximum temperature T in vacuum, which was used during complete poling, was kept $< 115 \sim 135^\circ\text{C}$ depending on Curie temperature of BaTiO_3 crystals.

In the series of conduction measurement of the surface with $P_s \leftrightarrow$, poling $T \sim 120^\circ\text{C}$, $P_s \downarrow$, poling $T \sim 120^\circ\text{C}$, $P_s \leftrightarrow$, poling $T \sim 120^\circ\text{C}$, $P_s \downarrow$, poling $T \sim 120^\circ\text{C}$, $P_s \leftrightarrow$, poling $T \sim 120^\circ\text{C}$, $P_s \uparrow$, poling $T \sim 120^\circ\text{C}$, $P_s \leftrightarrow$, poling $T \sim 120^\circ\text{C}$, and $P_s \uparrow$ [5], where arrows $\uparrow \downarrow \rightarrow$ show the P_s directions, the conductance of the same direction was the same. That is, the conductance is determined by P_s , while it had increased with time if heating affected the conductance.

The weakness of this experiment was the absence of h^+ conduction for $P_s \downarrow$. The h^+ conduction and other properties supporting the e^-/h^+ layer by polar discontinuity were confirmed in subsequent experiments [7]. Therefore, the predictions of Fig. 1 [5] and their basis [6] are considered appropriate. In addition, single domains far wider than existing theories were formed agreeing with the prediction [6].

V. IMPLICATION OF POLAR-DISCONTINUITY CARRIER LAYER

Section IV supports the prediction of e^-/h^+ carrier layers due to polar-discontinuity, although in many cases these layers form with aids of defects. Therefore, we examine the impacts of the e^-/h^+ carrier layers on the depolarization field in ideal ferroelectrics without defects. Figures 3(a) and 3(b) are the results for PbTiO_3 with an infinite band gap E_g (no e^-/h^+ carriers) and $E_g = 3.2 \text{ eV}$, respectively [6, 31]. PbTiO_3 was chosen, because it has a large Free energy of ferroelectric phase and hence is resistive against depolarization-field instability.

First, critical thickness changed drastically; it is 500 nm and $< 10 \text{ nm}$ for an infinite and finite E_g , respectively, when the insulator thickness l_d is $0.77\epsilon_d \text{ nm}$ (ϵ_d : relative permittivity of insulator, Fig. 3). Here, the induced charge Q in Si electrode is due to P_s . Therefore, P_s is stable as far as $Q \neq 0$, irrespective of the value of Q . Secondly, in Fig. 3(b), $Q \ll P_s$ for $l_d > 0.25\epsilon_d \text{ nm}$, in striking contrast with Fig. 3(a). This is because P_s is mostly screened by e^-/h^+ carrier layers inside PbTiO_3 .

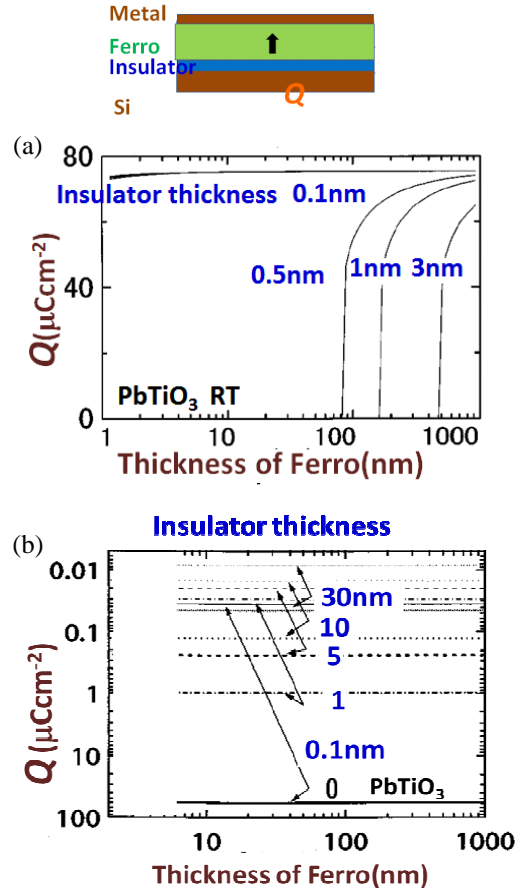


Fig. 3 The critical thickness that is the thickness for the induced charge $Q = 0$ and Q vs. ferroelectric (PbTiO_3) thickness with different SiO_2 ($\epsilon_d = 3.9$) for an infinite E_g (a) and a finite E_g (b) (adopted from Phys. Rev. B57, 789(1998) [5] and J. Appl. Phys. 83, 2179 (1998) [31]). p -type PbTiO_3 is considered. Therefore, screening is more efficient for $P_s \downarrow$ than for $P_s \uparrow$ and two lines exist for a given insulator thickness in Fig. 3(b): Q is smaller for $P_s \downarrow$ than for $P_s \uparrow$.

VI. CONCLUDING REMARKS

Ferroelectric field effect devices, which have been intensively extensively studied by many excellent researchers in industry and academics [28], are rare experiments in physics that provide quantitative properties such as induced charge for given insulator and ferroelectric thickness. Therefore, these results are not only useful for possible industrializations but also as fundamental data of ferroelectricity under depolarization field.

Thanks to these data, a theory of which predictions are consistent with many recent experiments appeared [5, 6]. It predicted (1) e^-/h^+ layer by polar discontinuity such as ferroelectric/insulator interface, ferroelectric free-surface, and charged domain boundaries, (2) diminished depolarization instability and size effects, and (3) freer domain configurations.

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